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wherein the first driver has a master IC for driving a first group of the first electrodes, and at least one slave IC for driving a second group of the first electrodes;

wherein the master IC has a display control signal generation section which generates a display control signal based on a signal from an external MPU; and wherein each of the master IC and the at least one slave IC has an input terminal for receiving the display control signal output from the display control signal generation section of the master IC through an external wiring.

7. (Amended) An electronic apparatus including an electro-optical device comprising:

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a display section which includes a plurality of first electrodes extending in a first direction, a plurality of second electrodes extending in a second direction crossing the first direction, and electro-optical elements driven by the first and second electrodes;

a first driver which drives the first electrodes; and

a second driver which drives the second electrodes,

wherein the first driver has a master IC for driving a first group of the first electrodes and at least one slave IC for driving a second group of the first electrodes; wherein the master IC comprises:

a display control signal generation section which generates a display control signal based on a signal from an external MPU;

an internal delay circuit which delays the display control signal; and

an output terminal which outputs the display control signal before the display control signal passes through the internal delay circuit; and

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wherein the at least one slave IC has an input terminal for receiving the display control signal output from the output terminal of the master IC through an external wiring.

Please add the following new claims 16-18:

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16. (New) The electro-optical device as defined in claim 4, wherein each of the master IC and the at least one slave IC comprises: a display memory into which display data from the external MPU is written; a display address circuit which assigns a display address for the display data which is read out from the display memory and displayed in the display section; and a driver which supplies a data signal based on the display data read out from the display memory to the first electrodes, and wherein the display control signal input through the input terminal is supplied to the display address circuit and the driver.

17. (New) The electro-optical device as defined in claim 4, wherein a gray scale display is performed in the display section based on a pulse width modulation signal output from the master IC and the at least one slave IC; and wherein the display control signal generated in the display control signal generation section includes a gray scale control pulse for generating the pulse width modulation signal.

18. (New) The electro-optical device as defined in claim 7, wherein the signal delay in the internal delay circuit is variable.